

India's 1st Chip design course by



अनवरत शिक्षा केंद्र - भारतीय प्रौद्योगिकी संस्थान रुड़की
Continuing Education Centre - Indian Institute of Technology Roorkee

तेजस True Insight of
Semiconductor Design

Powered by CEC IIT Roorkee in partnership with VLSI industry stalwarts

in Collaboration with



About TEJAS

Tejas, a distinctive semiconductor training curriculum developed in collaboration with India's most trusted education centers, CEC IIT Roorkee and VLSI industry stalwarts, aims to equip aspiring engineers with expertise in cutting-edge front-end and back-end VLSI technologies.



Salary
best salary package!



Live Classes
6 Months



100% Placement Assistance*
by Truechip Solutions Pvt. Ltd.



Certificate
Association with
industry partner

Key Highlights

- ✓ Certificate course in association with Industry and CEC IIT Roorkee
- ✓ Training by Industry expert trainers
- ✓ 24x7 Support by Truechip Solutions
- ✓ 1:1 Personalised Mentoring
- ✓ Flexible course schedule
- ✓ Opportunity to attend IEEE conference across the Globe
- ✓ Free Access to Learning Management System worth \$ 300/-
- ✓ Get 3+ Live Project
- ✓ Live case studies
- ✓ 100% Placement Assistance by Truechip Solutions
- ✓ Free Access to VLSI Industry leading tools
- ✓ 1 Month Campus Immersion

Who Can Apply for the Course?

- ✓ Individuals with a bachelor's degree and a keen interest to learn VLSI Chip Design.
- ✓ Professionals and academicians looking for a career transition as VLSI Chip Design Engineer
- ✓ Professionals aiming to move ahead in their career.
- ✓ Freshers who aspire to build their career in the field of VLSI Chip Design.

Application Process

The application process consists of three simple steps. Candidates have to submit their application. An offer of admission will be made to the selected candidates, and their application will be accepted upon the payment of admission fee.

1



SUBMIT APPLICATION

Tell us a bit about yourself and why you want to join

2



ADMISSION TEST

Clear the Aptitude, Digital & C Language based admission test

3



ADMISSION LETTER

Shortlisted candidates would be offered with the admission letter

NOTE:- Decision taken by CEC IIT Roorkee Management in regard to test/interview/selection - will be final

Program Curriculum(DV)

MODULE 1

Problem solving with C and C++

MODULE 2

Advance Digital Design

MODULE 3

System Verilog- HDVL

MODULE 4

Scripting Language (Python)

MODULE 5

Self-Paced

MODULE 6

FPGA Architecture

MODULE 7

CMOS Fundamentals

MODULE 8

Verilog HDL

MODULE 9

Universal Verification Methodology

MODULE 10

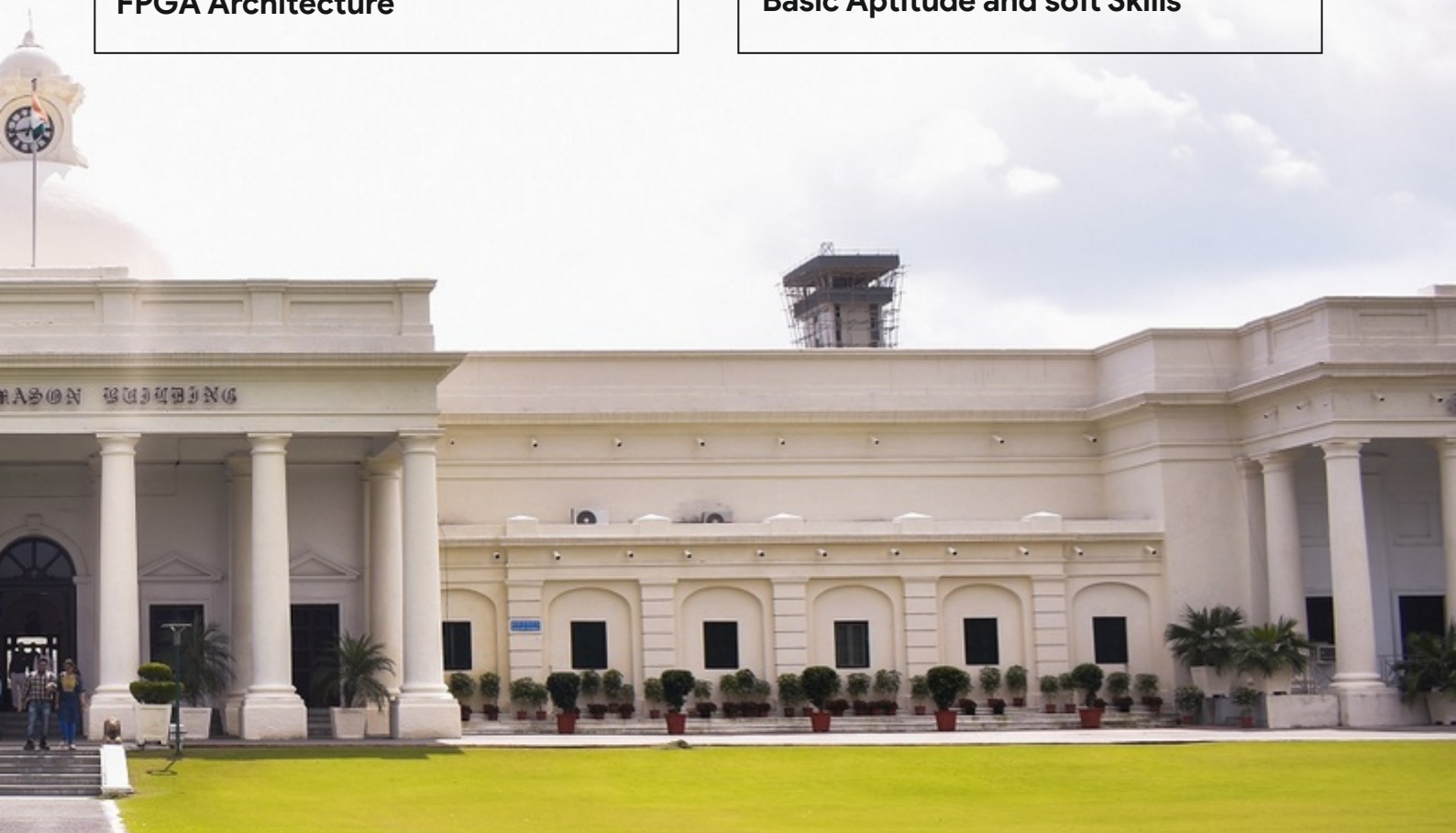
Spec Study (Industry Level Protocols)

MODULE 11

Industry Oriented Project

MODULE 12

Basic Aptitude and soft Skills



TEJAS

OTHER COURSES

TOPICS	TEJAS	OTHER COURSES
Best salary package	✓	✗
Placement Assistance	✓	✗
Project Based Learning	✓	Available with some
10+ years experience Trainers	✓	✗
International Internship (option)	✓	✗
LMS Access	✓	Available with some
Industry leading tools	✓	Available with some
Three month internship	✓	✗
Opportunity to attend VLSI conferences across globe	✓	✗

This Comparison is based on the information from the internet search available on the public domain

PLACEMENT PARTNERS



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