

# India's 1st Chip design course by



भारतीय प्रौद्योगिकी संस्थान रुड़की  
Indian Institute of Technology Roorkee



True Insight of  
Semiconductor Design

Affiliation with IIT Roorkee and helmed by FutureWiz

in Collaboration with



# HOTTEST JOB OF 21ST CENTURY-VLSI CHIP DESIGN INDUSTRY



## 5 Million Jobs Postings

There is global Estimate of Five million job postings for VLSI Engineers in this year



## Skill Development

Engineers are Equipped with various relevant VLSI Chip Design skills, Fetches the lucrative job offers



## Growth in VLSI Industry

10.11% CAGR in Indian Semiconductor Industry



## Future-oriented Career

IC Design is a growing industry, and sooner you join the band wagon the high you will reach

## Government Thrust: Way Forward



India and the US have entered into an agreement to set up a semiconductor fabrication plant to make chips for use in “national security, next generation telecommunications and green energy applications”, according to the Indo-US joint fact-sheet.



# About TEJAS

Tejas, a distinctive semiconductor training curriculum developed in collaboration with India's most trusted education centers, IIT Roorkee and FutureWiz, aims to equip aspiring engineers with expertise in cutting-edge front-end and back-end VLSI technologies.



**Salary**  
best salary package  
or money back!



**Live Classes**  
6 Months



**100% Placement Assistance\***  
Assured 6 digit salary



**Certification**  
Association with  
industry partner

## Key Highlights

- ✓ Certification course in association with Industry and IIT Roorkee
- ✓ Training by Industry expert trainers
- ✓ 24X7 Support
- ✓ 1:1 Personalised Mentoring
- ✓ Flexible course schedule
- ✓ Opportunity to attend IEEE conference across the Globe
- ✓ Free Access to Learning Management System worth \$ 300/-
- ✓ Get 3+ Live Project
- ✓ Live case studies
- ✓ 100% Placement Assistance
- ✓ Free Access to VLSI Industry leading tools



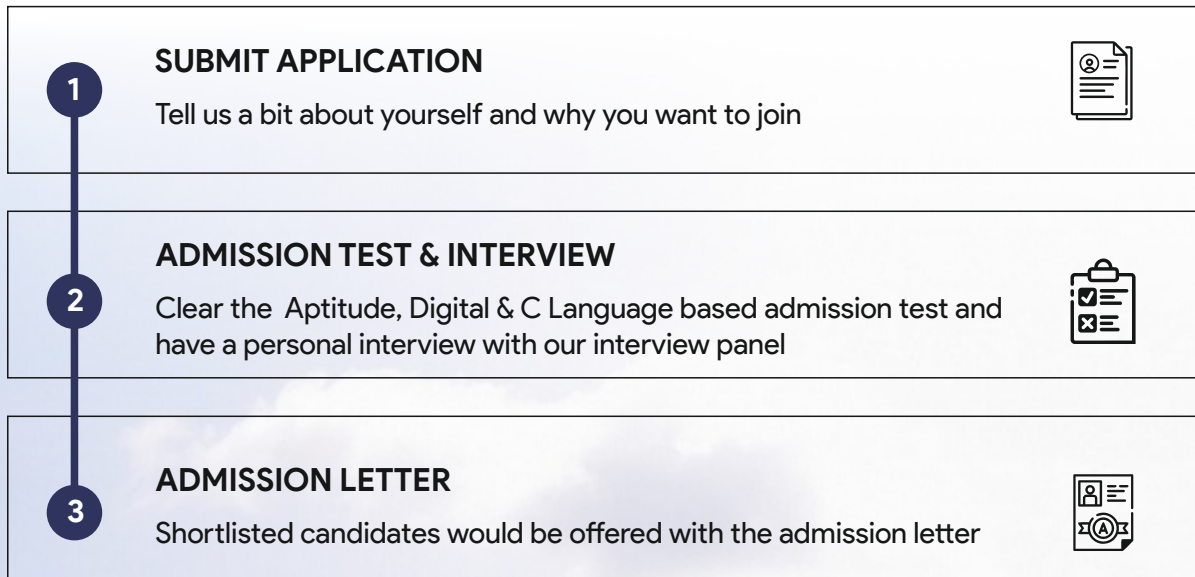
# Who Can Apply for the Course?

- ✓ Individuals with a bachelor's degree and a keen interest to learn VLSI Chip Design.
- ✓ Professionals and academicians looking for a career transition as VLSI Chip Design Engineer
- ✓ Professionals aiming to move ahead in their career.
- ✓ Freshers who aspire to build their career in the field of VLSI Chip Design.

## Application Process

The application process consists of three simple steps. Candidates have to submit their application.

An offer of admission will be made to the selected candidates, and their application will be accepted upon the payment of admission fee.



**NOTE:-** Decision taken by Futurewiz Management in regard to test/interview/selection - will be final



# Program Curriculum(DV)

## MODULE 1

**Advance Digital Design**

## MODULE 2

**Introduction to C and OOPS**

## MODULE 3

**Verilog HDL**

## MODULE 4

**System Verilog**

## MODULE 5

**UVM (Universal Verification Methodology)**

## MODULE 6

**FPGA Architecture**

## MODULE 7

**Introduction to Linux**

## MODULE 8

**CMOS Fundamentals**

## MODULE 9

**Interface and Protocols**

## MODULE 10

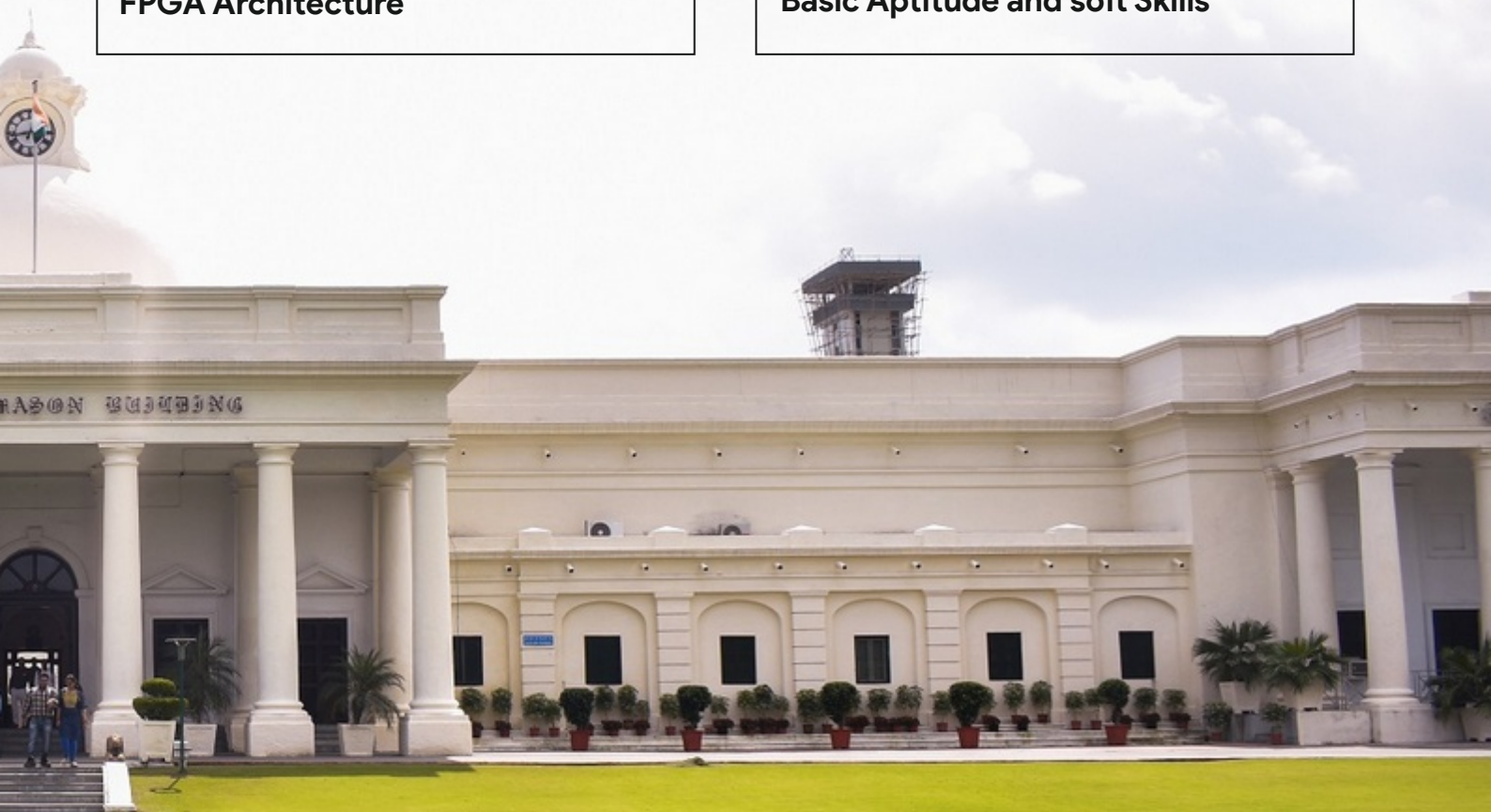
**Mini Project On RTL Design and Verification**

## MODULE 11

**Major Project On Verification Using UVM**

## MODULE 12

**Basic Aptitude and soft Skills**



# TEJAS

## OTHER COURSES

TOPICS	TEJAS	OTHER COURSES
Best salary package	✓	✗
Placement Assistance	✓	✗
Money Back Policy	✓	✗
Project Based Learning	✓	Available with some
10+ years experience Trainers	✓	✗
International Internship (option)	✓	✗
LMS Access	✓	Available with some
Industry leading tools	✓	Available with some
Two month internship	✓	✗
Opportunity to attend VLSI conferences across globe	✓	✗

This Comparison is based on the information from the internet search available on the public domain

## PLACEMENT PARTNERS



\*All the trademarks are related to their respective owners.

Contact us @ +91 9999418031